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1. (12 points total) *Porpourri*

(a) (4 points)

The shortest time to completion first scheduling algorithm yields the minimum average response time for a set of jobs in a uniprocessor system. Why is it not used in all computer systems ?

(b) (4 points)

Access lists and capabilities are two mechanisms for protection. Explain briefly how each of these mechanisms works.

(c) (4 points)

How can the virtual memory address mapping mechanism be used to improve the performance of sending large messages in an operating system which uses message passing ?

2. (16 points total) *Virtual Memory*

Many machines have a translation lookaside buffer (TLB). A TLB is a cache of page table information. It stores the most recently used virtual to physical page mappings. When translating a virtual address, the address translation hardware looks up the TLB for the required mapping. If the mapping is in the TLB, no access to the page table is necessary. This improves performance because access to the TLB is faster than access to the page table.

(a) (4 points) Give two examples where the existence of the TLB requires operating system code which would be unnecessary on the same machine without a TLB.

(b) (4 points)

What is the benefit of including an address space identifier (as well as a virtual address) for each entry in the TLB ?

(c) (8 points)

Some machines (for example the VAX) do not have a reference bit. It is possible however, to simulate a reference bit using software and the valid bit. Describe how this can be done.

3. (12 points total) *File Systems*

(a) (6 points)

Two file systems are identical except in the way that they allocate disk blocks to files. System 1 uses a linked list for block allocation. A block is allocated from the head of the list. When it is released, it is added to the end of the list. System 2 uses

